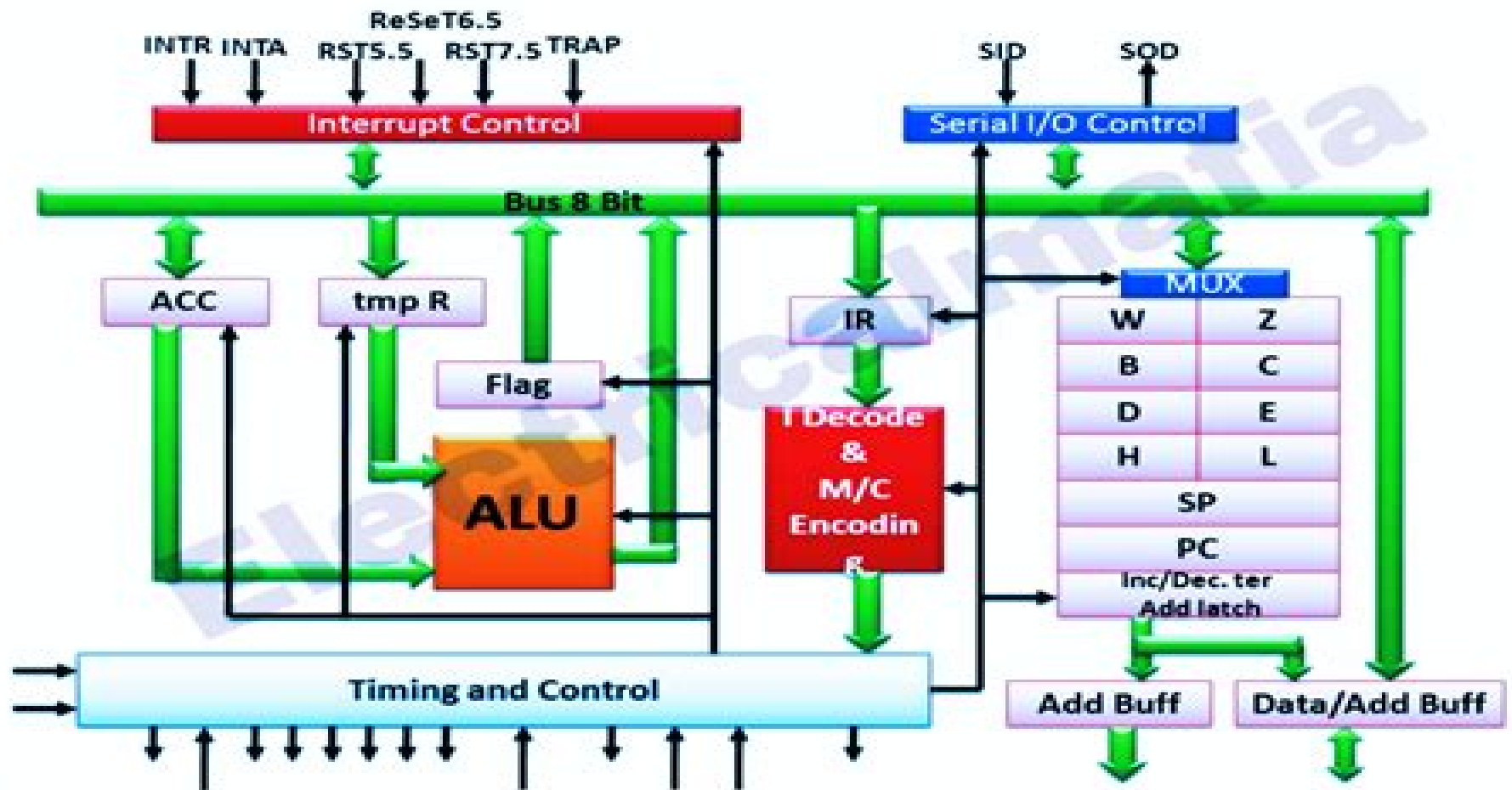


8085 Microprocessor Architecture



Microprocessor 8085 Microprocessor 8085

N. K. SRINATH



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The 8085 Microprocessor K. Udaya Kumar, 2008 Designed for an undergraduate course on the 8085 microprocessor this text provides comprehensive coverage of the programming and interfacing of the 8 bit microprocessor Written in a simple and easy to understand manner this book introduces the reader to the basics and the architecture of the 8085 microprocessor It presents balanced coverage of both hardware and software concepts related to the microprocessor

Microprocessor 8085, 8086 Abhishek Yadav, 2008 *Microprocessor 8085 and Its Interfacing* Sunil Mathur, 2011-09 This comprehensive and thoroughly updated text now in its second edition continues to provide the complete knowledge about the Intel s 8085 microprocessors its programming and concept of interfacing of memory input output devices and programmable peripheral chips Organized in four parts Part I Chapters 1 9 covers a review of the analog and digital signals as well as hardware and software related aspects of microprocessor 8085 Part II Chapters 10 and 11 discusses memory and input output concepts analog to digital and digital to analog converters and various memory and IO address decoding techniques Part III Chapters 12 17 explains the programmable interfacing chips with extensive interfacing examples Part IV Chapters 18 and 19 presents a brief discussion on other 8 bit microprocessors along with 16 and 32 bit Intel Processors Each topic has been supported with numerous examples that will help students apply the concepts to other microprocessors in the course at advanced level This book is designed specifically for the undergraduate students of electronics and communication engineering computer science and engineering and information technology New to this Edition Chapters on Architecture and Organization of Microprocessor and Instruction Set of 8085 Microprocessor have been revised and modified substantially Multiple choice questions have been added to all the chapters **Ten Days with 8085 MICROPROCESSOR** K. A.

KRISHNAMURTHY, Krishnamurthy K. A., 2010-01-01 This text is designed to provide hands on experience to students to help them develop an understanding of the hardware components of a microprocessor and the role of software in programming and interfacing aspects of the microprocessor An 8 bit microprocessor due to its simpler instruction set and architecture is an ideal IC chip for providing the students with a solid foundation for micro processors their principles and applications The concepts of all state of the art processors can be understood easily once the basics of the 8085 are understood Today s sophisticated microprocessors have a semblance of 8085 The presentation style adopted in this book in a way is unique It is a student friendly text written as conversation between the teacher and the students The book lucidly explains the various programming examples in assembly language with a view to enabling students to develop microprocessor based industrial application projects Application programs developed in the book are based on the popular microprocessor kit namely SDA 85 The book is suitable for both diploma and degree level students pursuing courses in Electronics and Electrical Engineering Electronics and Communication Engineering and Information and Communication Technology **MICROPROCESSOR 8085** Dr. Rekha Annigeri, 2017-06-22 The 8085 microprocessor introduced by Intel in 1976 is an 8 bit microprocessor that

forms the core of many educational and embedded system projects. It operates on an 8 bit data bus and a 16 bit address bus which allows it to access up to 64KB of memory. The 8085 microprocessor is known for its simplicity making it a popular choice for those new to microprocessor programming and design. Its architecture includes 74 instructions and supports operations such as data transfer, arithmetic, logical branching and control instructions. The 8085 microprocessor is equipped with five 8 bit registers B, C, D, E, H and L, a 16 bit stack pointer and a 16 bit program counter. These registers facilitate data manipulation and address handling within the processor. The accumulator, an essential part of the 8085, is an 8 bit register that plays a critical role in arithmetic and logical operations. The processor operates at a clock speed of 3 MHz, which was quite advanced for its time, allowing it to perform up to 0.5 MIPS (Million Instructions Per Second). One of the significant features of the 8085 microprocessor is its interrupt system, which provides five interrupt inputs, allowing it to respond to external events promptly. These interrupts are vectored, meaning they automatically branch to specific memory locations to execute the interrupt service routines. This feature is particularly useful in real time applications where the microprocessor needs to handle multiple tasks simultaneously. The 8085 also includes a Serial Input/Output control, which is essential for communication with peripheral devices. This feature allows the microprocessor to be integrated into more complex systems, enabling it to communicate with other devices and systems effectively. The simplicity of the 8085's instruction set and architecture makes it an excellent tool for learning the fundamentals of microprocessor programming and understanding the basic principles of how microprocessors interact with memory and peripheral devices. Its legacy continues in educational settings where it serves as a foundation for understanding more advanced microprocessor architectures.

MICROPROCESSOR 8085 AJAY WADHWA, 2010-01-04 This book is designed as a first level introduction to Microprocessor 8085 covering its architecture, programming and interfacing aspects. Microprocessor 8085 is the basic processor from which machine language programming can be learnt. The text offers a comprehensive treatment of microprocessor's hardware and software. Distinguishing features: All the instructions of 8085 processor are explained with the help of examples and diagrams. Instructions have been classified into groups and their mnemonic hex codes have been derived. Memory maps of different memory sizes have been illustrated with examples. Timing diagrams of various instructions have been illustrated with examples. A large number of laboratory tested programming examples and exercises are provided in each chapter. At the end of each chapter, numerous questions and problems have been given. Problems from previous years' question papers have been separately given in each chapter. More than 200 examples and problems have been covered in the entire text. This book is designed for undergraduate courses in B.Sc. Hons. Physics and B.Sc. Hons. Electronics. It will also be useful for the students pursuing B.Tech. degree/diploma in electrical and electronics engineering. *Microprocessor Architecture, Programming and Applications with the 8085* Ramesh Gaonkar, 2002-11 **Microprocessor Architecture, Programming, and Applications with the 8085** Ramesh S. Gaonkar, 1996 Revised to include the most recent

technological changes this comprehensive survey offers an integrated treatment of both the hardware and software aspects of the microprocessor focusing on the 8085 microprocessor family to teach the basic concepts underlying programmable devices Providing a sound pedagogy from basic concepts to applications it prepares users to apply concepts learned to a variety of situations they may encounter in their future jobs Microprocessor 8085 Junied Khalid Khan,2018-06-30 I am writing this book for my fellow students who are preparing for NET GATE This book contains to the point information about the microprocessor 8085 without going through the unnecessary details that are not relevant to your exam I hope it will help you to go through the concepts and then apply these concepts to solve problems at the end *8085 MICROPROCESSOR* N. K. SRINATH,2005-01-01 This up to date and contemporary book is designed as a first level undergraduate text on micro processors for the students of engineering computer science electrical electronics telecommunication instrumentation computer applications and information technology It gives a clear exposition of the architecture programming and interfacing and applications of 8085 microprocessor Besides it provides a brief introduction to 8086 and 8088 Intel microprocessors The book focusses on microprocessors starting from 4004 to 80586 instruction set of 8085 microprocessor giving the clear picture of the operations at the machine level the various steps of the assembly language program development cycle the hardware architecture of microcomputer built with the 8085 microprocessor the role of the hardware interfaces memory input output and interrupt in relation to overall microcomputer system operation peripheral chips such as 8255 8253 8259 8257 and 8279 to interface with 8085 microprocessor and to program it for different applications

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