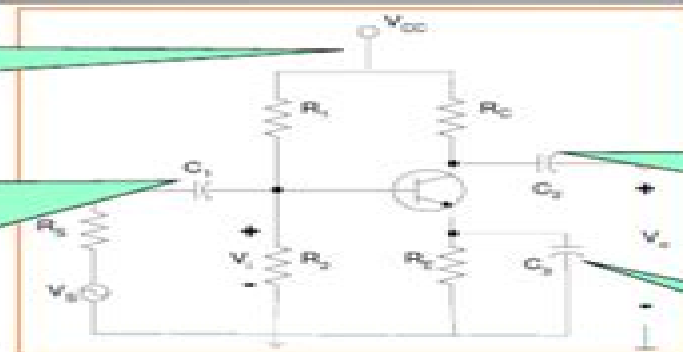


BJT TRANSISTOR MODELING

DC supply $\rightarrow$ "0" potential

• I/p coupling capacitor $\rightarrow$ s/c
• Large values
• Block DC and pass AC signal



Voltage-divider configuration under AC analysis

• O/p coupling capacitor $\rightarrow$ s/c
• Large values
• Block DC and pass AC signal

• Bypass capacitor $\rightarrow$ s/c
• Large values



Redraw the voltage-divider configuration after removing dc

Modeling The Bipolar Transistor Computer Aided Design Of Electronic Circuits

L. Richard Carley, Ronald Gyurcsik



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Richard Carley, Ronald Gyurcsik, 2012-12-06 Computer Aided Design of Analog Circuits and Systems brings together in one place important contributions and state of the art research results in the rapidly advancing area of computer aided design of analog circuits and systems This book serves as an excellent reference providing insights into some of the most important issues in the field Analogue Electronic Circuits and Systems Amitava Basak, 1991-11-29 This book is an undergraduate

textbook for students of electrical and electronic engineering It is written with second year students particularly in mind and discusses analogue circuits used in various fields **Computer-aided Design of Microelectronic Circuits and Systems:**

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Narain D. Arora, 2012-12-06 Metal Oxide Semiconductor MOS transistors are the basic building block of MOS integrated circuits I C Very Large Scale Integrated VLSI circuits using MOS technology have emerged as the dominant technology in the semiconductor industry Over the past decade the complexity of MOS IC s has increased at an astonishing rate This is realized mainly through the reduction of MOS transistor dimensions in addition to the improvements in processing Today VLSI circuits with over 3 million transistors on a chip with effective or electrical channel lengths of 0.5 microns are in volume production Designing such complex chips is virtually impossible without simulation tools which help to predict circuit behavior before actual circuits are fabricated However the utility of simulators as a tool for the design and analysis of circuits depends on the adequacy of the device models used in the simulator This problem is further aggravated by the technology trend towards smaller and smaller device dimensions which increases the complexity of the models There is extensive literature available on modeling these short channel devices However there is a lot of confusion too Often it is not clear what model to use and which model parameter values are important and how to determine them After working over 15 years in the field of semiconductor device modeling I have felt the need for a book which can fill the gap between the theory and the practice of MOS transistor modeling This book is an attempt in that direction Fundamentals and Standards in Hardware Description Languages Jean Mermet, 2012-12-06

The second half of this century will remain as the era of proliferation of electronic computers They did exist before but they were mechanical During next century they may perform other mutations to become optical or molecular or even biological Actually all these aspects are only fancy dresses put on mathematical machines This was always recognized to be true in the domain of software where machine or high level languages are more or less rigorous but immaterial variations of the universally accepted mathematical language aimed at specifying elementary operations functions algorithms and processes But even a mathematical machine needs a physical support and this is what hardware is all about The invention of hardware description languages HDL s in the early 60 s was an attempt to stay longer

at an abstract level in the design process and to push the stage of physical implementation up to the moment when no more technology independent decisions can be taken. It was also an answer to the continuous exponential growth of complexity of systems to be designed. This problem is common to hardware and software and may explain why the syntax of hardware description languages has followed with a reasonable delay of ten years the evolution of the programming languages: at the end of the 60s they were Algol like, a decade later Pascal like, and now they are C or ADA like. They have also integrated the new concepts of advanced software specification languages.

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