

# **Modeling in Analog Design**

**Edited by**

**Jean-Michel Bergé, Oz Levia and Jacques Rouillard**

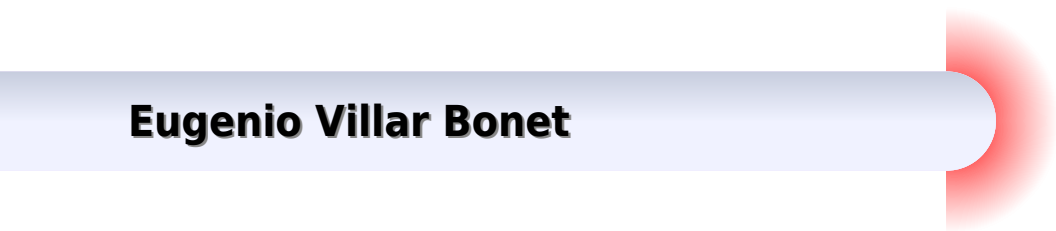


**Current Issues in Electronic Modeling**

**Kluwer Academic Publishers**

# Modeling In Analog Design Current Issues In Electronic Modeling

**Eugenio Villar Bonet**



## **Modeling In Analog Design Current Issues In Electronic Modeling:**

**Modeling in Analog Design** Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2012-12-06 Modeling in Analog Design highlights some of the most pressing issues in the use of modeling techniques for design of analogue circuits Using models for circuit design gives designers the power to express directly the behaviour of parts of a circuit in addition to using other pre defined components There are numerous advantages to this new category of analog behavioral language In the short term by favouring the top down design and raising the level of description abstraction this approach provides greater freedom of implementation and a higher degree of technology independence In the longer term analog synthesis and formal optimisation are targeted Modeling in Analog Design introduces the reader to two main language standards VHDL A and MHDL It goes on to provide in depth examples of the use of these languages to model analog devices The final part is devoted to the very important topic of modeling the thermal and electrothermal aspects of devices This book is essential reading for analog designers using behavioral languages and analog CAD tool development environments who have to provide the tools used by the designers

*Object-Oriented Modeling* Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2012-12-06 Object oriented techniques and languages have been proven to significantly increase engineering efficiency in software development Many benefits are expected from their introduction into electronic modeling Among them are better support for model reusability and flexibility more efficient system modeling and more possibilities in design space exploration and prototyping Object Oriented Modeling explores the latest techniques in object oriented methods formalisms and hardware description language extensions The seven chapters comprising this book provide an overview of the latest object oriented techniques for designing systems and hardware Many examples are given in C VHDL and real time programming languages Object Oriented Modeling describes further the use of object oriented techniques in applications such as embedded systems telecommunications and real time systems using the very latest techniques in object oriented modeling It is an essential guide to researchers practitioners and students involved in software hardware and system design

The Electronic Design Automation Handbook Dirk Jansen, 2010-02-23 When I attended college we studied vacuum tubes in our junior year At that time an average radio had ve vacuum tubes and better ones even seven Then transistors appeared in 1960s A good radio was judged to be one with more thententransistors Later good radios had 15 20 transistors and after that everyone stopped counting transistors Today modern processors runing personal computers have over 10 million transistors and more millions will be added every year The difference between 20 and 20M is in complexity methodology and business models Designs with 20 tr sistors are easily generated by design engineers without any tools whilst designs with 20M transistors can not be done by humans in reasonable time without the help of Prof Dr Gajski demonstrates the Y chart automation This difference in complexity introduced a paradigm shift which required sophisticated methods and tools and introduced design automation into design practice By the decomposition of the design process into many tasks and

abstraction levels the methodology of designing chips or systems has also evolved Similarly the business model has changed from vertical integration in which one company did all the tasks from product specification to manufacturing to globally distributed client server production in which most of the design and manufacturing tasks are outsourced

*Models in System Design* Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2012-12-06 *Models in System Design* tracks the general trend in electronics in terms of size complexity and difficulty of maintenance System design is by nature combined with prototyping mixed domain design and verification and it is no surprise that today's modeling and models are used in various levels of system design and verification In order to deal with constraints induced by volume and complexity new methods and techniques have been defined *Models in System Design* provides an overview of the latest modeling techniques for use by system designers The first part of the book considers system level design discussing such issues as abstraction performance and trade offs There is also a section on automating system design The second part of the book deals with some of the newest aspects of embedded system design These include co verification and prototyping Finally the book includes a section on the use of the MCSE methodology for hardware software co design *Models in System Design* will help designers and researchers to understand these latest techniques in system design and as such will be of interest to all involved in embedded system design

**Analog and Mixed-Signal Hardware Description Language** A. Vachoux, Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2012-12-06 Hardware description languages HDL such as VHDL and Verilog have found their way into almost every aspect of the design of digital hardware systems Since their inception they gradually proved to be an essential part of modern design methodologies and design automation tools ever exceeding their original goals of being description and simulation languages Their use for automatic synthesis formal proof and testing are good examples So far HDLs have been mainly dealing with digital systems However integrated systems designed today require more and more analog parts such as A/D and D/A converters phase locked loops current mirrors etc The verification of the complete system therefore asks for the use of a single language Using VHDL or Verilog to handle analog descriptions is possible as it is shown in this book but the real power is coming from true mixed signal HDLs that integrate discrete and continuous semantics into a unified framework Analog HDLs AHDL are considered here a subset of mixed signal HDLs as they intend to provide the same level of features as HDLs do but with a scope limited to analog systems possibly with limited support of discrete semantics *Analog and Mixed Signal Hardware Description Languages* covers several aspects related to analog and mixed signal hardware description languages including The use of a digital HDL for the description and the simulation of analog systems The emergence of extensions of existing standard HDLs that provide true analog and mixed signal HDLs The use of analog and mixed signal HDLs for the development of behavioral models of analog electronic building blocks operational amplifier PLL and for the design of microsystems that do not only involve electronic parts The use of a front end tool that eases the description task with the help of a graphical paradigm yet generating AHDL descriptions automatically *Analog and Mixed Signal Hardware*

Description Languages is the first book to show how to use these new hardware description languages in the design of electronic components and systems It is necessary reading for researchers and designers working in electronic design

Model Engineering in Mixed-Signal Circuit Design Sorin Alexander Huss,2006-04-18 For the first time this up to date text combines the main issues of the hardware description language VHDL AMS aimed at model representation of mixed signal circuits and systems characterization methods and tools for the extraction of model parameters and modelling methodologies for accurate high level behavioural models

**Hardware/Software Co-Design and Co-Verification** Jean-Michel Bergé,Oz Levia,Jacques Rouillard,2013-03-09 Co Design is the set of emerging techniques which allows for the simultaneous design of Hardware and Software In many cases where the application is very demanding in terms of various performances time surface power consumption trade offs between dedicated hardware and dedicated software are becoming increasingly difficult to decide upon in the early stages of a design Verification techniques such as simulation or proof techniques that have proven necessary in the hardware design must be dramatically adapted to the simultaneous verification of Software and Hardware Describing the latest tools available for both Co Design and Co Verification of systems Hardware Software Co Design and Co Verification offers a complete look at this evolving set of procedures for CAD environments The book considers all trade offs that have to be made when co designing a system Several models are presented for determining the optimum solution to any co design problem including partitioning architecture synthesis and code generation When deciding on trade offs one of the main factors to be considered is the flow of communication especially to and from the outside world This involves the modeling of communication protocols An approach to the synthesis of interface circuits in the context of co design is presented Other chapters present a co design oriented flexible component data base and retrieval methods a case study of an ethernet bridge designed using LOTOS and co design methodologies and finally a programmable user interface based on monitors Hardware Software Co Design and Co Verification will help designers and researchers to understand these latest techniques in system design and as such will be of interest to all involved in embedded system design

**High-Level System Modeling** Ronald Waxman,Jean-Michel Bergé,Oz Levia,Jacques Rouillard,2012-12-06 In system design generation of high level abstract models that can be closely associated with evolving lower level models provides designers with the ability to incrementally test an evolving design against a model of a specification Such high level models may deal with areas such as performance reliability availability maintainability and system safety Abstract models also allow exploration of the hardware versus software design space in an incremental fashion as a fuller detailed design unfolds leaving behind the old practice of hardware software binding too early in the design process Such models may also allow the inclusion of non functional aspects of design e g space power heat in a simulatable information model dealing with the system s operation This book addresses Model Generation and Application specifically in the following domains Specification modeling linking object data modeling behavior modeling and activity modeling Operational specification modeling modeling

the way the system is supposed to operate from a user's viewpoint Linking non functional parameters with specification models Hybrid modeling linking performance and functional elements Application of high level modeling to hardware software approaches Mathematical analysis techniques related to the modeling approaches Reliability modeling Applications of High Level Modeling Reducing High Level Modeling to Practice High Level System Modeling Specification and Design Methodologies describes the latest research and practice in the modeling of electronic systems and as such is an important update for all researchers design engineers and technical managers working in design automation and circuit design

Meta-Modeling Jean-Michel Bergé, Oz Levia, Jacques Rouillard, 2012-12-06 Models in system design follow the general tendency in electronics in terms of size complexity and difficulty of maintenance While a model should be a manageable representation of a system this increasing complexity sometimes forces current CAD tool designers and model writers to apply modeling techniques to the model itself Model writers are interested in instrumenting their model so as to extract critical information before the model is complete CAD tools designers use internal representations of the design at various stages The complexity has also led CAD tool developers to develop formal tools theories and methods to improve relevance completeness and consistency of those internal representations Information modeling involves the representation of objects their properties and relationships Performance Modeling When it comes to design choices and trade offs performance is generally the final key However performance estimations have to be extracted at a very early stage in the system design Performance modeling concerns the set of tools and techniques that allow or help the designer to capture metrics relating to future architectures Performance modeling encompasses the whole system including software modeling It has a strong impact on all levels of design choices from hardware software partitioning to the final layout Information Modeling Specification and formalism have in the past traditionally played little part in the design and development of EDA systems their support environments languages and processes Instead EDA system developers and EDA system users have seemed to be content to operate within environments that are often extremely complex and may be poorly tested and understood This situation has now begun to change with the increasing use of techniques drawn from the domains of formal specification and database design This section of this volume addresses aspects of the techniques being used In particular it considers a specific formalism called information modeling which has gained increasing acceptance recently and is now a key part of many of the proposals in the EDA Standards Roadmap which promises to be of significance to the EDA industry In addition the section looks at an example of a design system from the point of view of its underlying understanding of the design process rather than through a consideration of particular CAD algorithms Meta Modeling Performance and Information Modeling contains papers describing the very latest techniques used in meta modeling It will be a valuable text for researchers practitioners and students involved in Electronic Design Automation

**2nd Workshop on Libraries, Component Modeling and Quality Assurance** Eugenio Villar Bonet, 1997 Compendio de los trabajos presentados en

Toledo durante el 2nd Workshop on Libraries component modeling and quality assurance

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